

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernd Deutschmann, et al. Art Unit : 2836
Serial No. : 10/588,984 Examiner : Dharti H. Patel
Filed : November 21, 2006 Conf. No. : 4272
Title : CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN
INTEGRATED SEMICONDUCTOR CIRCUIT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

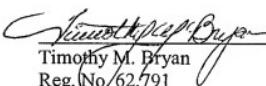
PETITION FOR TWO-MONTH EXTENSION OF TIME UNDER 37 C.F.R. §1.136

Please extend the period for response to the action dated March 12, 2009, for two months to and including August 12, 2009.

The petition fee in the amount of \$490 is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply all charges or credits to Deposit Account No. 06-1050, referencing Attorney Docket No. 14603-0026US1.

Respectfully submitted,

Date: August 12, 2009



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